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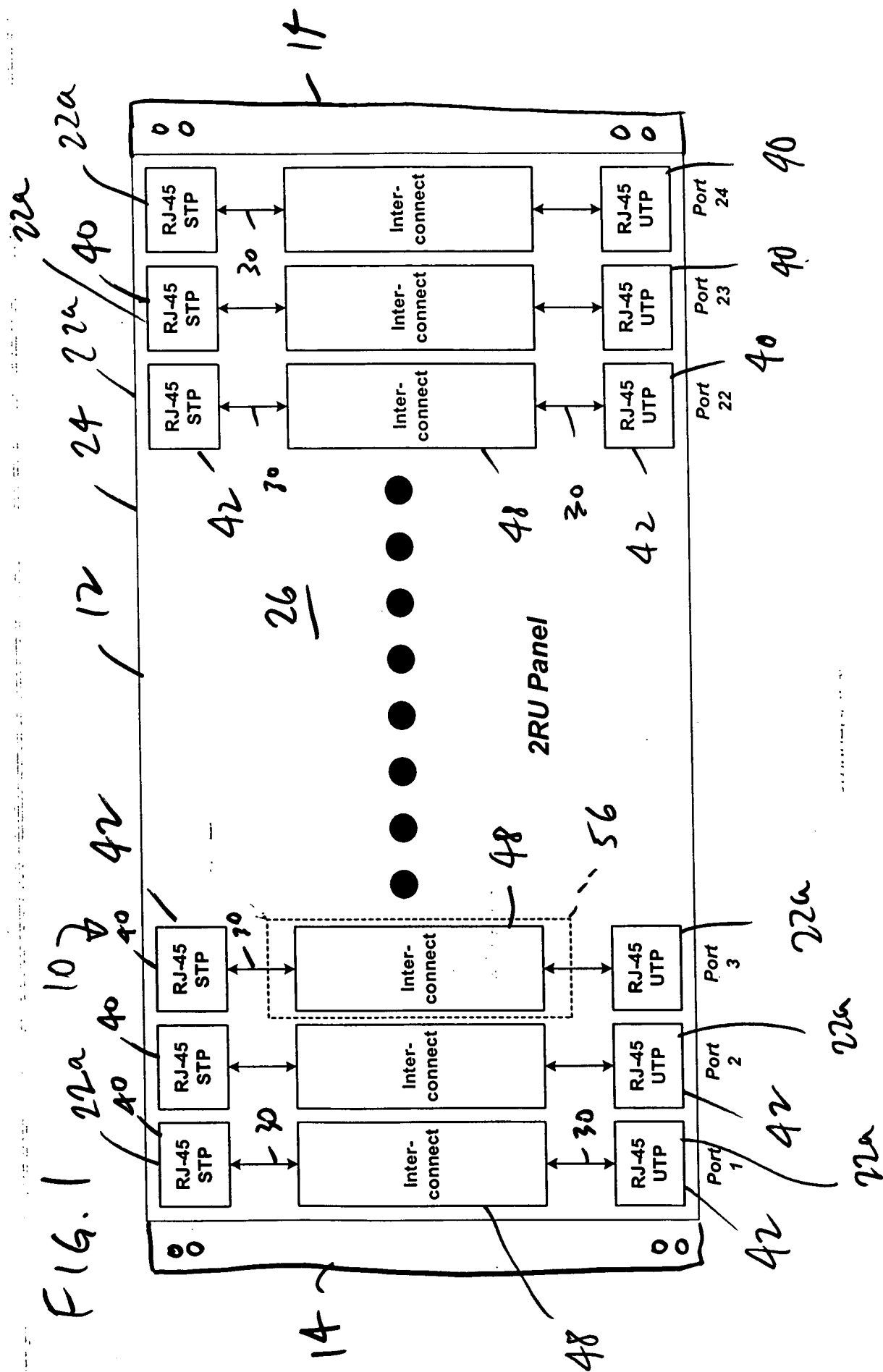


FIG. 2 107 20

Rear Mounted Optional Modules
Power and SNMP CPU

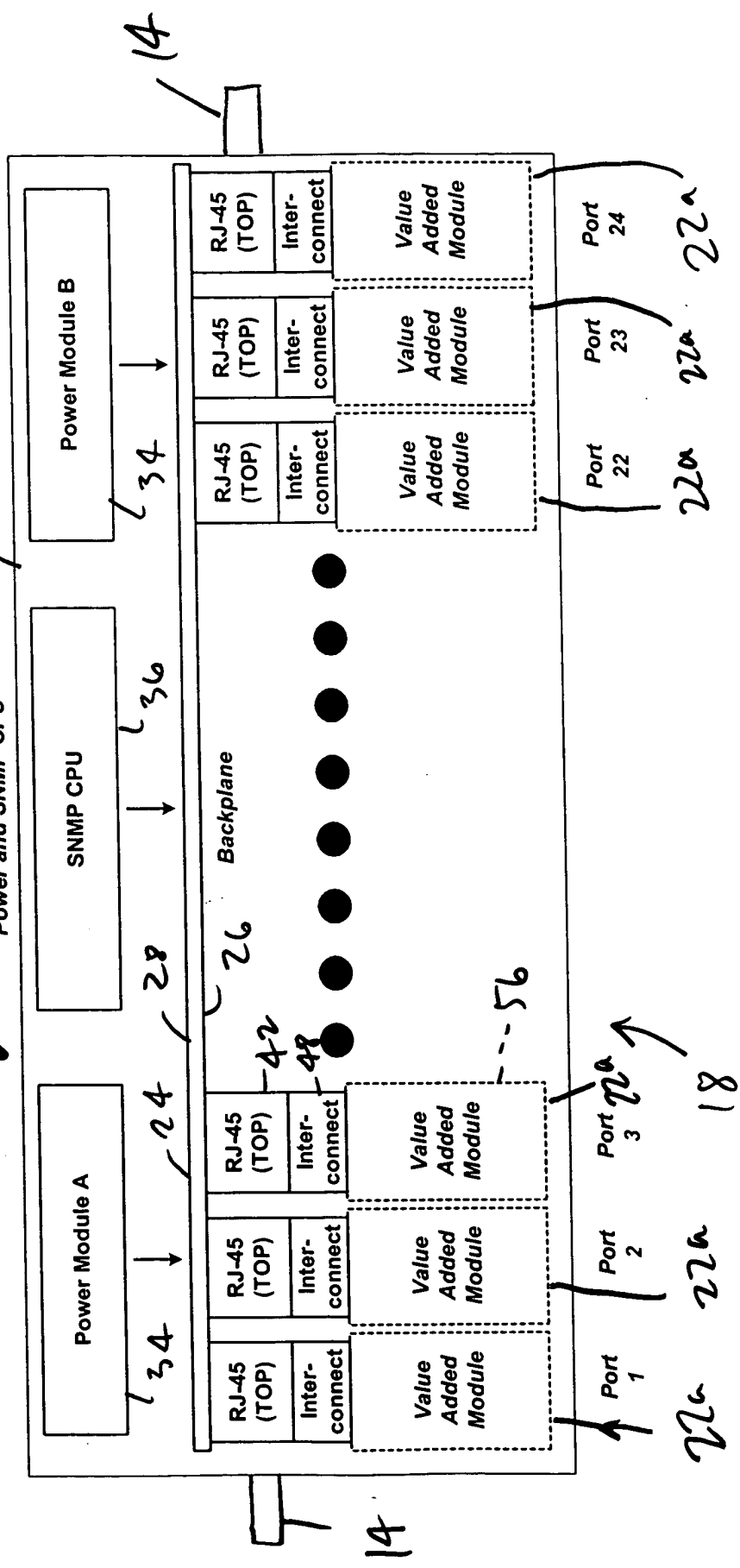


FIG. 3A 22a

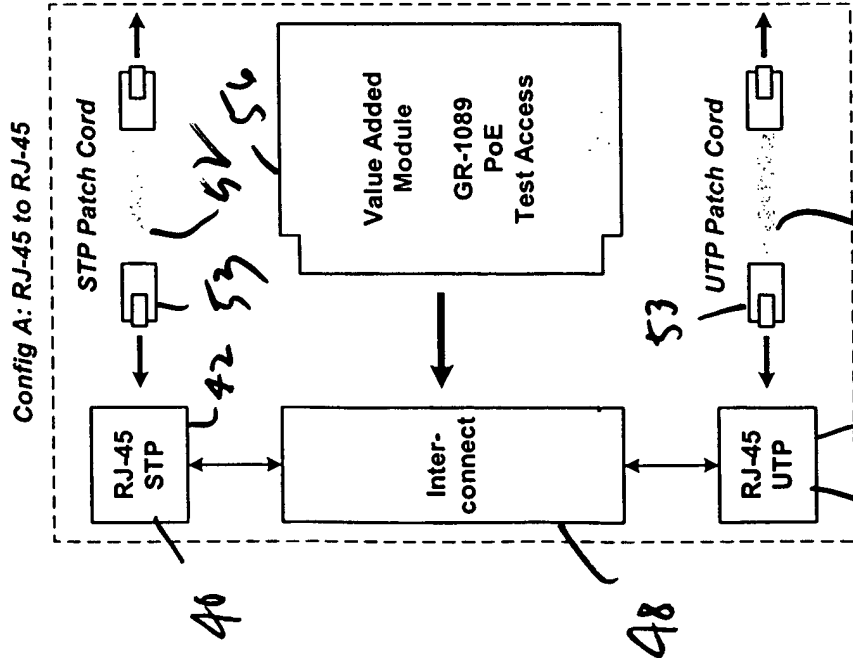


FIG. 3B 22b

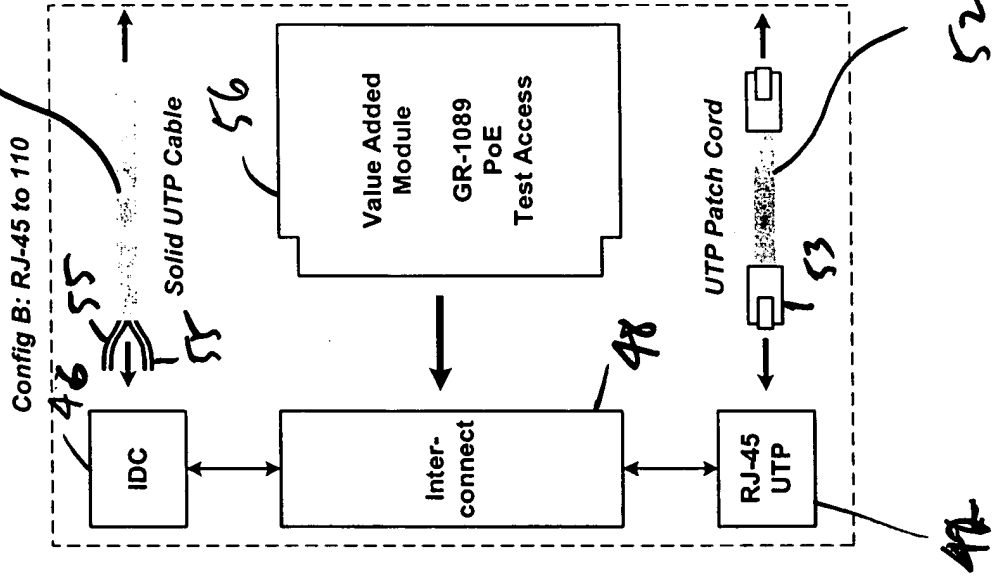
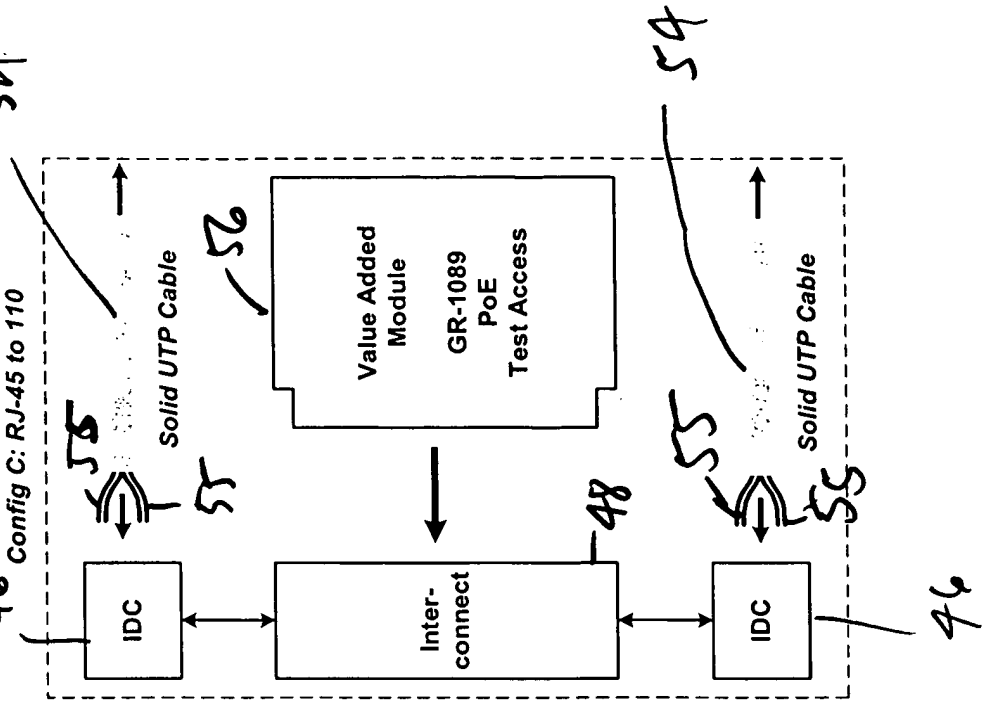


FIG. 3C 22c



24 46

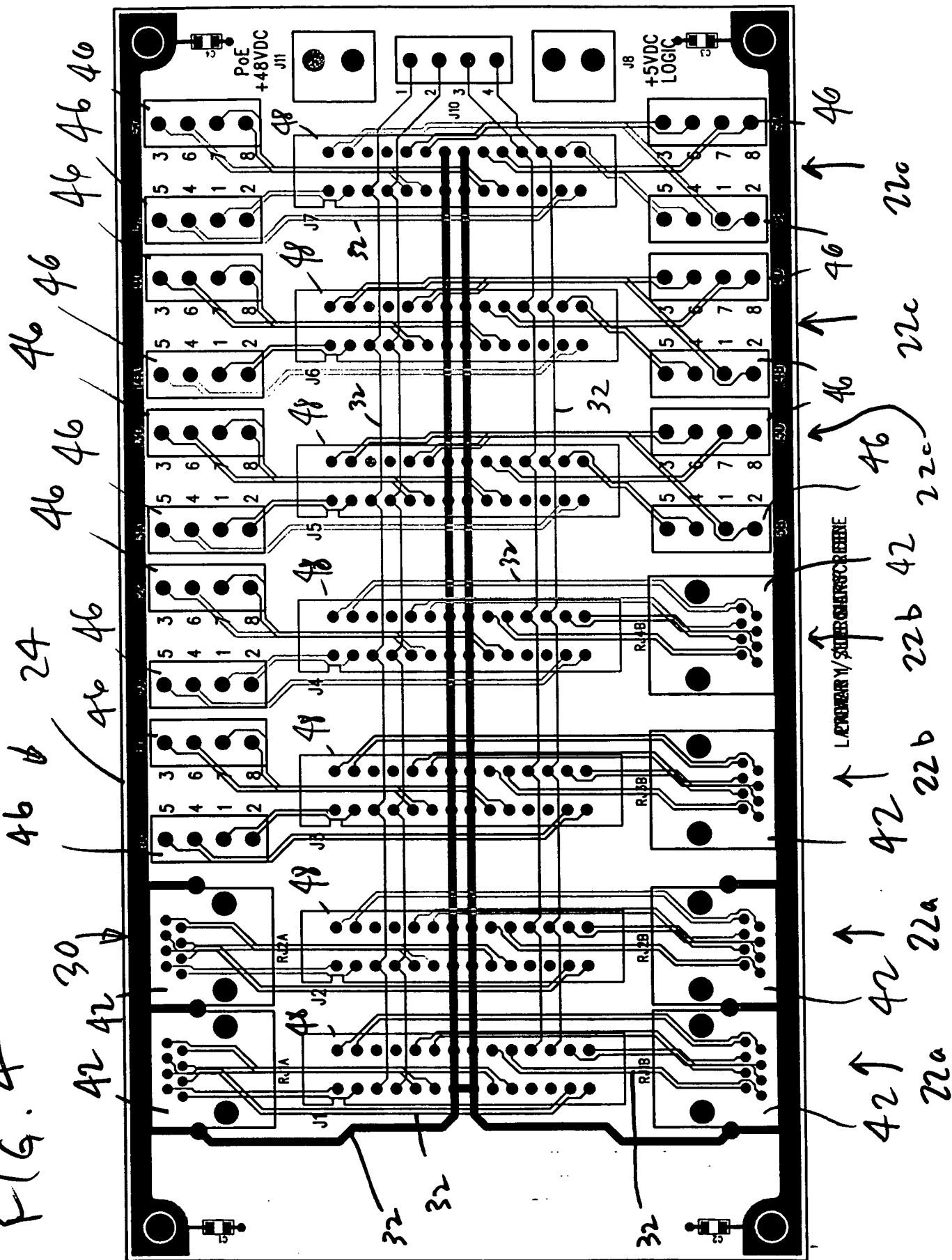


FIG. 5

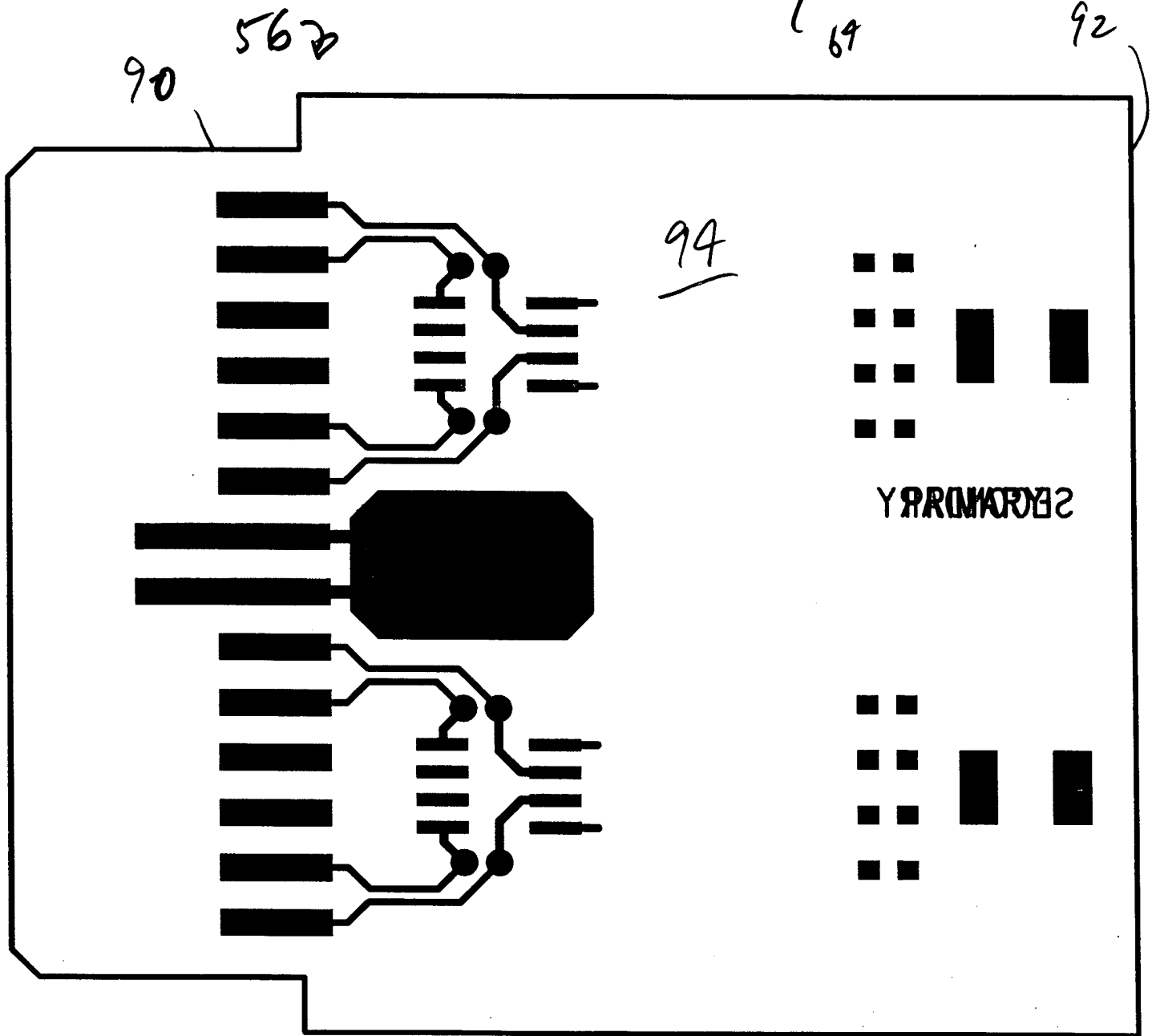


FIG. 7

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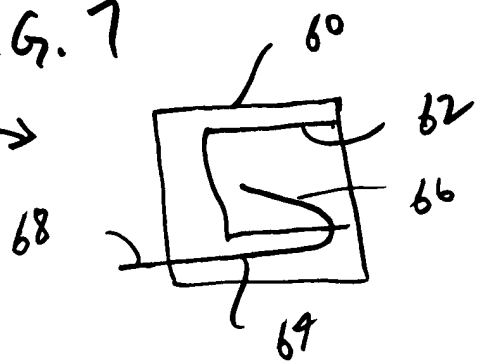


FIG. 6

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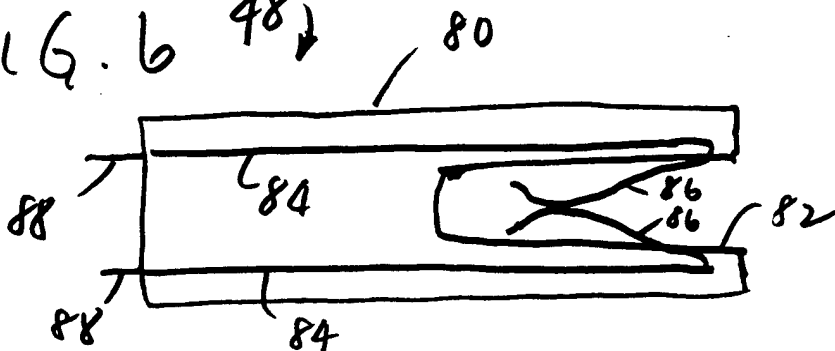
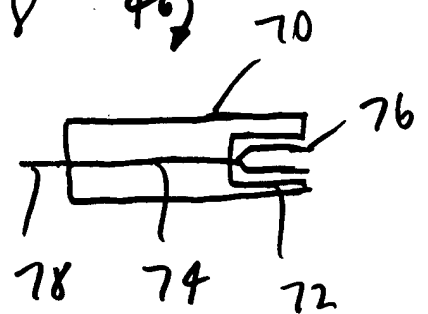
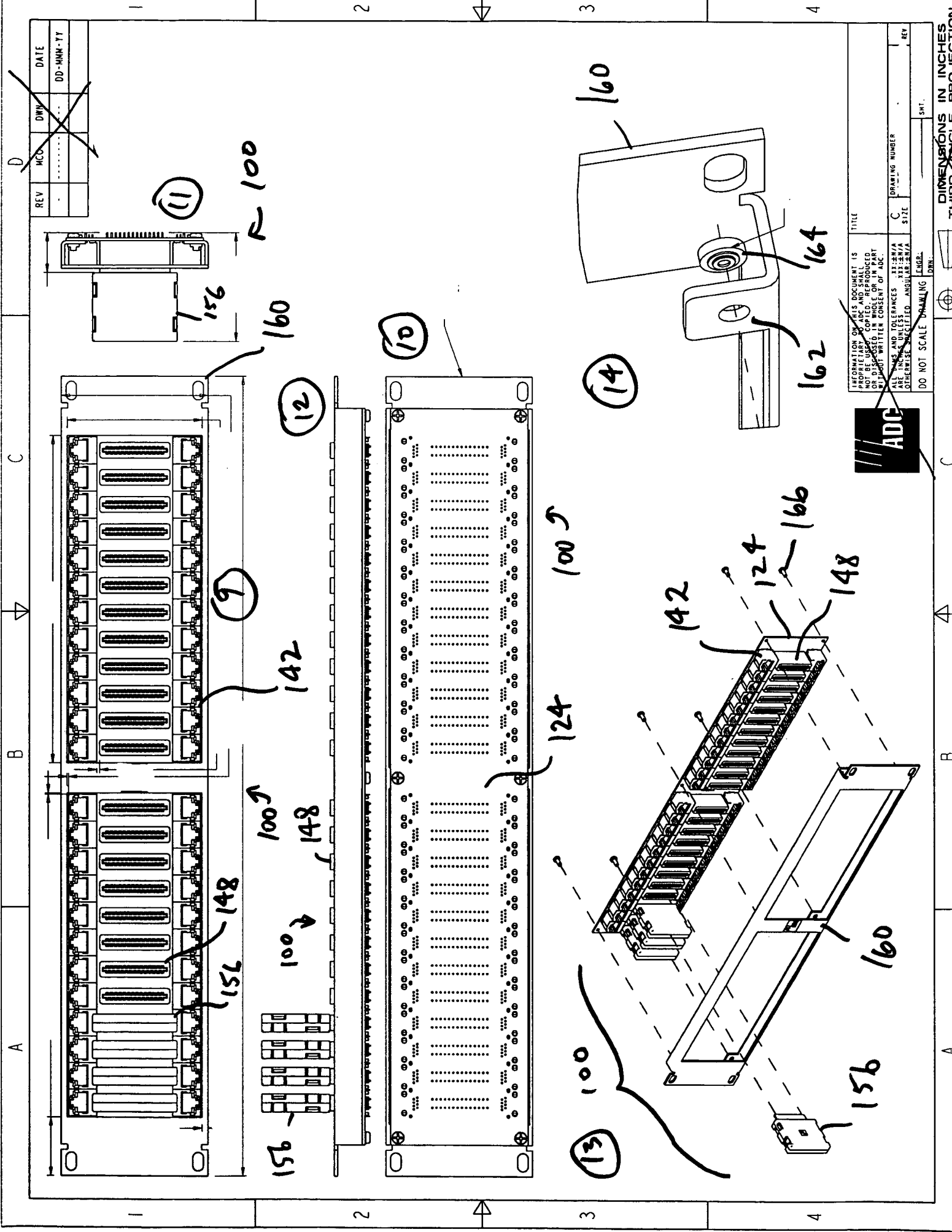
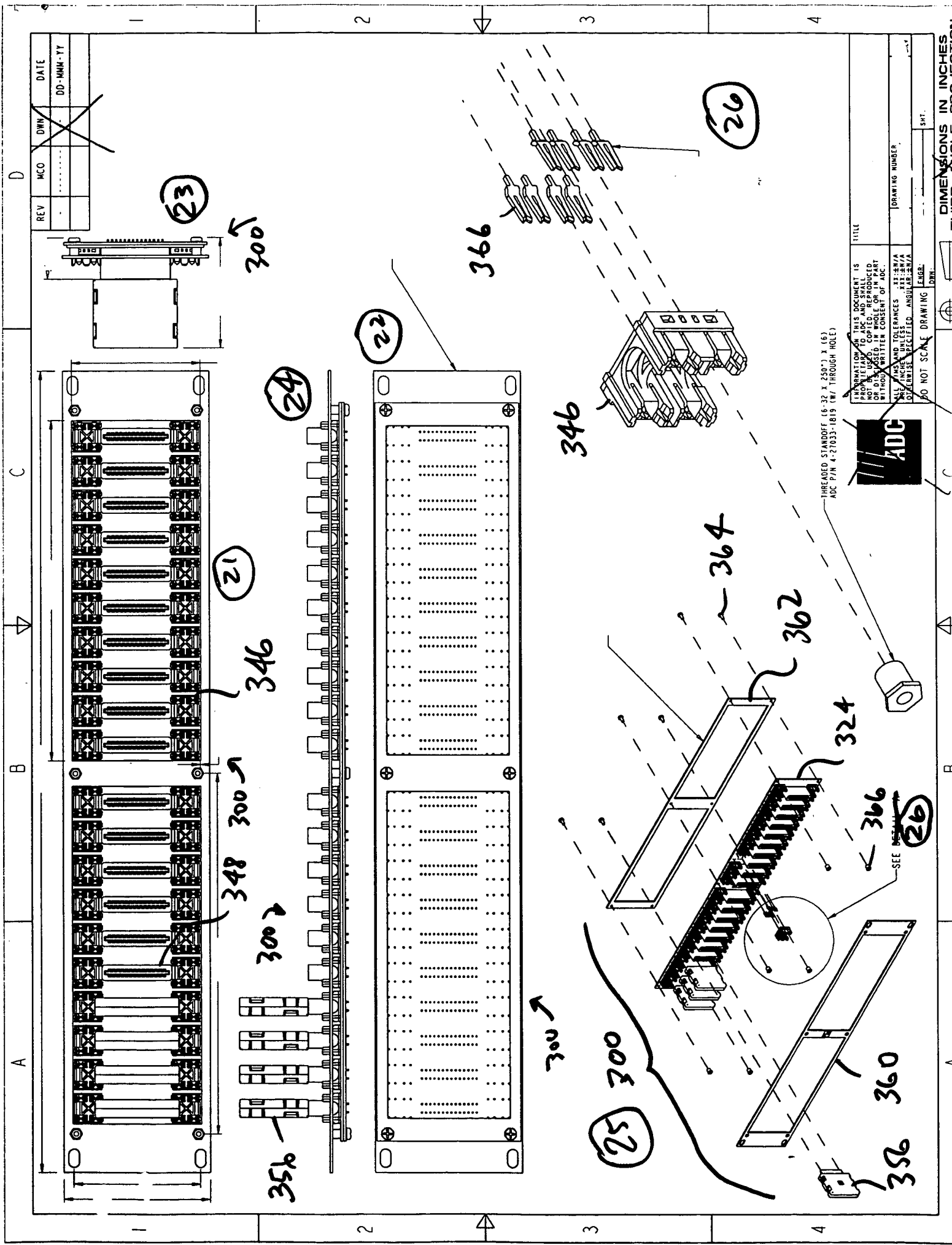


FIG. 8

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[illegible]



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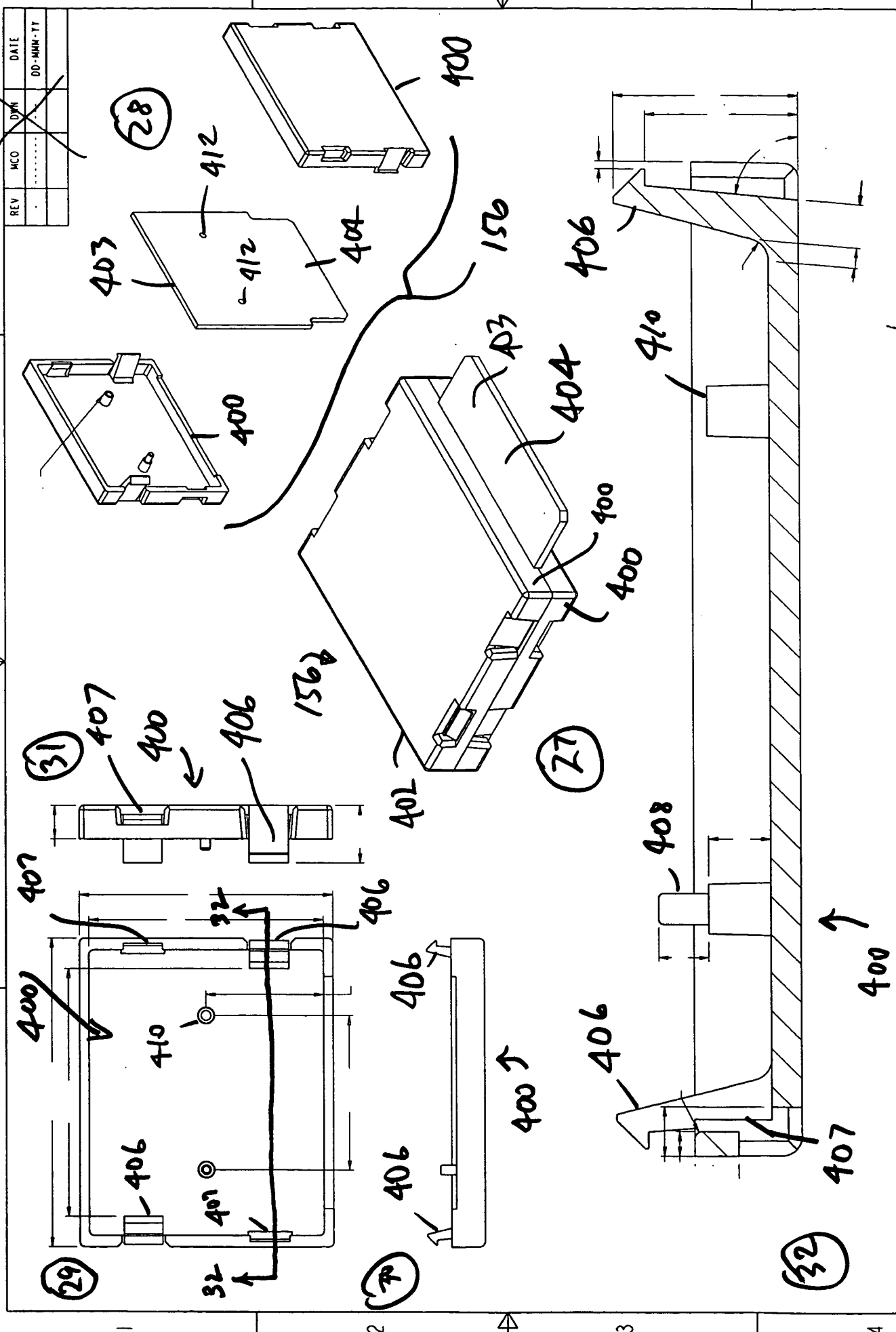
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